

PTO-1449

**Information Disclosure Statement
in an Application**

Application No.
09/747,492

Docket Number
068167.0105

Applicant(s)
Eric P. Traut

Group Art Unit
2423
2125

Filing Date
12/21/00

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FEB 05 2002
Group 2100

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
JW	A	5278973	1/11/94	O'Brien et al.	395	500	6/27/91
BW	B	5301277	4/5/94	Kanai	709	301	12/23/91
AM	C	5367628	11/22/94	Ote et al.	345	501	10/15/92
PT	D	5448264	9/5/95	Pinedo et al.	345	508	6/14/93
SW	E	5452456	9/19/95	Mourey et al.	713	100	12/18/92
JW	F	5502809	3/26/96	Takano	345	509	9/6/94
AW	G	5640562	6/17/97	Wold et al.	395	652	2/27/95
PD	H	5666521	9/9/97	Marisetty	345	525	11/20/96
JW	I	5742797	4/21/98	Celi, Jr. et al.	345	507	8/11/95
JW	J	5752275	5/12/98	Hammond	711	207	7/14/97
BW	K	5757386	5/26/98	Celi, Jr. et al.	345	507	8/11/95
JW	L	5790825	8/4/98	Traut	712	209	8/5/97
SW	M	5831607	11/3/98	Brooks	345	333	1/25/96
BW	N	5860147	1/12/99	Gochman et al.	711	207	9/16/96
SW	O	5940872	8/17/99	Hammond et al.	711	207	11/1/96
BW	P	6014170	1/11/00	Pont et al.	348	232	6/20/97
BW	Q	6026476	2/15/00	Rosen	711	206	8/6/97
BW	R	6067618	5/23/00	Weber	713	1	3/26/98

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

NON-PATENT DOCUMENTS

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)				
BW	S	"Processor Instruction Sets," The PC Guide, version date 12/18/00, http://www.pcguides.com/ref/cpu/arch/int/inst-c.html .				
BW	T	"M68060 User's Manual," Motorola, 1994, pp. i-xviii; Section 4, Memory Management Unit, pp. 4-1 to 4-30, http://e-www.motorola.com/bdata/PDFDB/MICROPROCESSORS/32_BIT/68K-COLDFIRE/M680X0/MC68060UM.pdf .				
BW	U	"MPC750, RISC Microprocessor User's Manual," Motorola, 8/97, Contents, pp. iii-xvi; Chapter 5, Memory Management, pp. 5-1 to 5-34; Glossary, pp. Glossary-1 to Glossary-13, http://e-www.motorola.com/bdata/PDFDB/MICROPROCESSORS/32_BIT/POWERPC/MPC7XX/MPC750UM.pdf .				

EXAMINER

Albert W. Paladini

ALBERT W. PALADINI
PATENT EXAMINER

DATE CONSIDERED


12-2-00

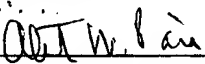
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. Patent and Trademark Office

HOU03:824653

HOU03:833550



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U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
DW	A	5,815,686	9/29/98	Earl, et al.	395	500	9/12/96
DW	B	4,779,188	10/18/88	Gum et al.	364	200	10/19/87
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
DW	C	EP 0 645 701 A2	26/09/94	EPO	G06F	9/455	X
NON-PATENT DOCUMENTS							
Technology Center 2100							
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					
DW	D	PCT International Search Report in International Application No. PCT/US 01/22276, International filing date 16/07/01, mail date 07/03/02.					
DW	E	Traut E, "Building the Virtual PC," <i>Byte</i> , McGraw-Hill Inc., Vol. 22, No. 11, pp. 51-52, 1 November 1997.					
GR	F	"Intel386 DX Microprocessor," <i>Intel</i> , pp. 32-58, 31 December 1995.					
GR	G	"Macintosh and Technology: Changing Chips in the Middle of the Stream, or Apple Takes a Risc," URL: www.btech.co/changingchips.html , paragraphs 00061-00071, retrieved 12/10/01.					
GR	H	"M68040 User's Manual," Motorola, Inc., Chapter 3, copyright 1990, revised 1992, 1993.					
DW	I	Osisek DL et al., "ESA/390 Interpretive-Execution Architecture, Foundation for VM/ESA," <i>IBM Systems Journal</i> , Vol. 30, No. 1, pp. 34-51, 1991.					
DW	J	Shang Rong Tsai et al., "On the Architectural Support for Logical Machine Systems," <i>Microprocessing and Microprogramming</i> , Vol. 22, No. 2, pp. 81-96, February 1988.					
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 ALBERT W. PALADINI PATENT EXAMINER					2-2-06		
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PTO-1449

**Information Disclosure Citation
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Application No.
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Applicant(s)
Eric P. Traut

Docket Number
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2123

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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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							YES	NO

NON-PATENT DOCUMENTS

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)				
um	A	U.S. Patent Application Serial No. 09/617,669, "System and Method for Displaying Current Images of Virtual Machine Environments", Traut, et al., filed July 17, 2000				
um	B	U.S. Patent Application Serial No. 09/617,624, "System and Method for Emulating the Operation of a Video Graphics Adapter", Carroll, et al., filed July 17, 2000				

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